ANNA UNIVERSITY

REGIONAL CAMPUS COIMBATORE

Advertisement for Junior Research Fellow (JRF) position in the Department of ECE

Chips to Startup Scheme, MeitY, Government of India

Applications are invited from Indian citizens for a temporary post of **Junior Research Fellow** for the **Chips to Startup (C2S) Project Scheme**, **MeitY, Government of India** in the Department of ECE, Anna University Regional Campus Coimbatore

Title of the project	ASIC Realization of Light Weight Hardware Security Protocol Architecture for RFID Enabled Applications
Description of the project:	The project intends to provide a physical design of digital hardware encoder architecture and implementation in ASIC and FPGA platform
Sponsored Agency	**MeitY, Government of India**
Project sanction number	MeitY vide computer No. 3080449 ; Dated: 10 January 2024
No. of posts	01 (one)
Duration	05 (Five) years or till Completion of the Project, whichever is earlier (extension of selected candidates would be based on performance review every 12 months)
Principal Investigator (P.I)	**Dr. V. R. Vijaykumar**

General Information and Terms & Conditions:

1.	Age Limit	**35 years (as of closing date of application, 30.03.2024)**
2.	Qualification	**Bachelor's Degree in Electronics and Communication Engineering / M.E/M.Tech in VLSI / Applied Electronics/Embedded Systems** or related area with first class with **valid GATE / NET Qualified.**
3.	Desirable Experience	The candidate should be highly proficient with digital design using Verilog/VHDL, basic programming in any language and should possess good understanding of FPGA design flow Working/Research Experience in the field of VLSI system design will be given preference.
4.	Salary	**INR. 37,000/-+HRA with GATE/NET Candidate[#]** **(# - Designation will be Junior Research Fellow)** **INR. 25,000/-+HRA without GATE/NET candidate*** (* - **Designation will be Project Associate I)**
5.	Relaxation on qualification and age	Relaxation as per Govt. of India Rules.

6.	Last date of receiving of filled application form (soft-copy)	**30.03.2024**
7.	Reporting	Applicants are to submit their filled-up applications along with C.V. to the PI accompanying photocopies of all relevant documents with self-attestation to the address: **Dr. V. R. Vijaykumar, Associate Professor, Department of Electronics and Communication Engineering, Anna University Regional Campus Coimbatore, Coimbatore–641046, Tamilnadu, India (Contact-9442014139).** Candidate should write "**Application for the post of JRF under the project MeitY vide computer No. 3080449**" on the top of the envelope. Applicants are also requested to send the soft copy of the duly filled application along with C.V. and all necessary documents to the PI through email (vrv@annauniv.ac.in) with the subject as "**Application for the post of JRF under the project MeitY vide computer No. 3080449**" on or before **30.03.2024**.
8.	General terms and Conditions	The Position is temporary and renewable each year project to satisfactory performance for a maximum of five years. The position is co-terminus with the project. Only short listed candidates will be communicated to appear in the interview and no other communication in this regard will be entertained. No TA/DA will be paid for attending the interview. M.E/M.Tech candidate selected for this position can apply for the regular Ph.D program in Anna University provided they satisfy the eligibility criterion for Ph.D program of Anna University.
9.	Date of interview, time, and venue	**Interview date and mode (online / offline) with other details will be duly notified through email.**
10.	No objection certificate	Working professional must submit a no-objection certificate from the present employer at the time of the interview.
11.	Institute right	The Institute has the discretion to restrict the number of candidates to be shortlisted for the interview based on screening. No candidate may be recruited if suitable candidates are not found.
12.	Signing of the agreement	Selected candidates shall have to sign an agreement at the time of joining.

ANNA UNIVERSITY REGIONAL CAMPUS COIMBATORE

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

APPLICATION FORM FOR THE POST OF JRF

1. Advertisement no _____dated:_____.

2. Title of the Project_____
    _____.

3. Name of the applicant (in block letters):

4. Father's/Husband's Name:

5. Date of Birth (DD-MM-YYYY):

6. Age as on **30.03.2024:**

7. Postal Address:

    E-mail: Phone no.:

8. Permanent Address:

9. Nationality:

10. Marital Status:

11. Whether belongs to SC/ST/PH/OBC: (If yes, attach certificate):

12. Educational Qualification: (Starting from Class X)

Exam. passed	Board/ University	Division	% age of marks/ CPI	Year of passing	Subjects

13. **NET/GATE Examinations Passed**:

I.NET			II.GATE		
Subject	**Qualifying date**	**Valid upto**	**Subject**	**Score**	**Valid upto**

14. **Details of employments**:

Sl. No.	Name of the Organization	Name of the post	Period	Scale of pay and present pay	Nature of duty

15. Publication Details:

16. Courses/Training in VLSI (provide proof):

17. Any other relevant information that you may like to furnish:

Declaration

I declare that the above information are true and correct to the best of my knowledge and belief.

Date: **Signature of the applicant**
Place